

EAST - [106-11370.wsp:1]

File View Edit Tools Window Help

☐ Pending
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- L1: (15) chireix near3 architecture
- L2: (9) 1 and predistortion
- L3: (9) 2 and (component adj signal)
- L4: (9) 3 and RF
- L5: (9) 4 and angle
- L6: (9) 5 and lookup
- L7: (9) 6 and entry
- L8: (9) 7 and interpolation
- L9: (9) 8 and decomposition
- L10: (9) 9 and processing
- L11: (9) 1 and predistortion.clm.
- L12: (9) 11 and (component adj signal).clm.
- L13: (9) 12 and interpolation.clm.
- L14: (4) 13 and decomposition.clm.
- L15: (4) 14 and RF.clm.
- L16: (4) 15 and lookup.clm.

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Series: L6 Browse Create Clear
 DBs: US-PG-PUB US-PAT EPO ☒ Purge
 Default operator: OR ☒ Highlight all hit terms initially

15 and lookup.clm.

☒ BRS form ☒ IS&R form ☒ Image ☒ Text ☒ HTML

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20050003770 A1	20050106	14	Predistortion circuit for a transmit system	455/114.3	455/114.2		Saed, Aryan	F
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050001678 A1	20050106	25	Adaptive predistortion for a transmit system with gain, pha	330/149			Saed, Aryan	F
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050001677 A1	20050106	23	Adaptive predistortion for a transmit system	330/149			Saed, Aryan	F
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6975167 B2	20051213	16	Adaptive predistortion for a transmit system with gain, pha	330/149	330/107; 330/136		Saed; Aryan	F

☒ Hits: ☒ Details: ☒ HTML

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- L13: (9) 12 and interpolation.clm.
- L14: (4) 13 and decomposition.clm.
- L15: (4) 14 and RF.clm.
- L16: (4) 15 and lookup.clm.
- L17: (3) chireix near3 architecture

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DBs: ☒ Plurals

Default operator: ☒ Highlight all hit terms initially

chireix near3 architecture

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6975167 B2	20051213	16	Adaptive predistortion for a transmit system with gain, pha	330/149	330/107; 330/136		Saed; Aryan
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6879209 B2	20050412	11	Switched-mode power amplifier using lumped eleme	330/251	330/207A		Grundlingh; Johan M.
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6836183 B2	20041228	7	Chireix architecture using low impedance amplifiers	330/124R	330/251		Wight; James Stuart

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